

## A VARIABLE MEMORY WIDTH ASSIGNMENT SYSTEM AND METHOD

ABSTRACT OF THE DISCLOSURE

A variable width memory system is disclosed. The variable width  
5 memory system facilitates efficient utilization of memory resources and  
delivery of information in a convenient manner. A plurality of memory  
locations store information and the bit widths of at least two of the memory  
locations are different. A controller directs access to the plurality of memory  
locations. Information is communicated between the controller and  
10 memory locations via a bus coupled to the controller and memory  
locations.

